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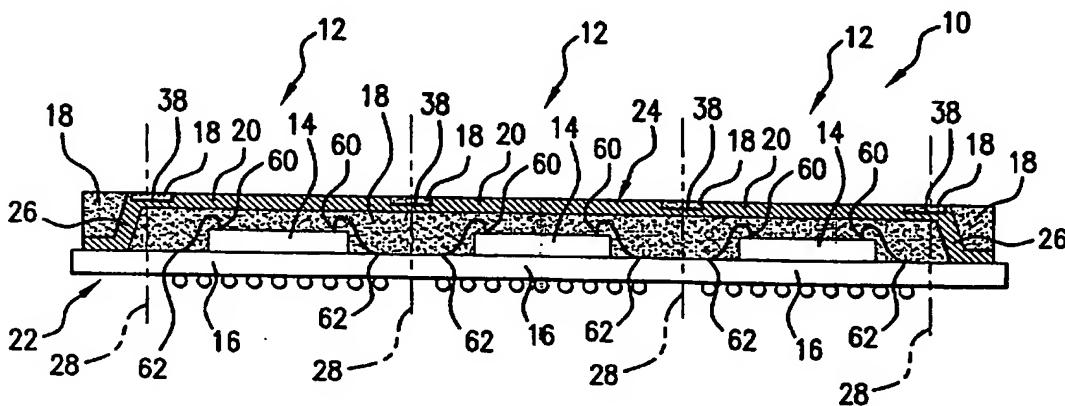
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(54) Title: THERMAL ENHANCED PACKAGE FOR BLOCK MOLD ASSEMBLY



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(57) Abstract: A heat spreader (20) is added to a package to enhance thermal and advantageously electrical performance. In manufacture, a heat spreader precursor (24) is advantageously placed over a group of dies and secured after bonding (e.g., wire or tape bonding or flip-chip bonding) and before matrix/block mold. For example, a package strip (10) may consist of a row (linear array) of groups of die attach areas (e.g. in a rectangular array of four). The heat spreader precursor (20) may accommodate one such group or multiple groups along the package strip (10). The package strip (10) may then be singulated to form the individual packages. Each singulated package includes a die (14), its associated substrate 16 (e.g., either a lead frame or interposer type substrate) and a portion of the heat spreader precursor (24) as a heat spreader (20).

THERMAL ENHANCED PACKAGE FOR BLOCK MOLD ASSEMBLY

CROSS REFERENCE TO RELATED APPLICATION

5 This application claims the benefit of U.S. Provisional Patent Application Number 60/415,189 filed on 30 September 2002, which is incorporated by reference herein in its entirety.

BACKGROUND OF THE INVENTION

10 1. Field of the Invention:

This invention relates to semiconductor device packages. More particularly, this invention relates to thermally enhanced, molded plastic semiconductor device packages.

15 2. Description of the Related Art:

Molded plastic packages provide environmental protection to integrated circuit devices (dies). Such packages typically include at least one semiconductor device (die) having its input/output (I/O) pads electrically connected to a lead frame type substrate or an interposer type substrate, with a molding compound coating the die and at least a portion of the substrate. Typically, the I/O pads on the die are electrically connected to bond sites on the substrate using either a wire bonding, tape bonding, or flip-chip bonding method. The lead frame or interposer substrate transmits electrical signals between the I/O pads and an electrical circuit external to the package.

In semiconductor device packages having a lead frame type substrate, electrical signals are transmitted between at least one die and external circuitry, such as a printed circuit board, by an electrically conductive lead frame. The lead frame includes a plurality of leads, each having an inner lead end and an opposing outer lead end. The inner lead end is electrically connected to the I/O pads on the die, and the outer lead end provides a terminal for connecting to the external circuitry. Where the outer lead end terminates at a face of the package body, the package is known as a "no-lead" or "leadless" package.

30 Examples of well-known no-lead packages include quad flat no-lead (QFN) packages, which have four sets of leads disposed around the perimeter of the bottom of a square package body, and dual flat no-lead (DFN) packages, which have two sets of leads disposed along opposite sides of the bottom of a package body.

In semiconductor device packages having an interposer type substrate, electrical signals are transmitted between at least one die and external circuitry, such as a printed circuit board, by a substrate comprising multiple layers, usually 2 or 3 thin layers, of dielectric material having electrical traces, pins, vias, and the like formed thereon. This 5 type of substrate is typically used in grid array packages, such as ball grid array (BGA) packages, pin grid array (PGA) packages, land grid array (LGA) packages, fine ball grid array (FBGA) packages, flexible ball grid array (FxBGA) packages, and any other type of package that requires lands on the package to be arranged at a suitable circuit board-attach pitch (a "grid array"). Solder balls, bumps or pins may be disposed on the lands, as in 10 BGA and PGA type packages, to facilitate connection to the circuit board.

In any type of molded plastic package, operation of the die generates heat that must be removed to maintain its operating integrity. While some heat is dissipated through the metallic components of the package, such as portions of the substrate and bond wires, the remainder is absorbed into the molding compound. Problematically, the molding 15 compound is a poor thermal conductor. As a result, attempts have been made to improve the thermal dissipation performance of the molded plastic package.

One way to increase thermal dissipation performance of a molded plastic package is to dispose a metallic heat spreader within the package. In one common design, the metallic heat spreader is placed below the die. Examples of such heat spreader designs are 20 provided, for example, in U.S. Patent Nos. 5,367,196 and 5,608,267, both to Mahulikar et al., and in U.S. Patent No. 5,650,663 to Parthasarathi, all of which are incorporated by reference herein in their entirety.

In another common design, the metallic heat spreader is spaced above the die, such that the die is positioned between the heat spreader and the substrate. In this design, the 25 heat spreader typically includes downwardly directed portions that contact the substrate, the die, or both. The downwardly directed portions may be adhered to the substrate and/or die using a dielectric adhesive. Such heat spreaders typically act to spread the heat throughout the encapsulant over an area larger than that of the chip. One example of such a heat spreader is provided in U.S. Patent No. 6,432,742 to Guan et al. Another example 30 of such a heat spreader is provided in U.S. Patent No. 5,977,626 to Wang et al.

Unfortunately, manufacturing methods for packages employing this type of heat spreader have not lent themselves to the high level of automation needed for assembly of lower cost molded plastic packages.

One attempt to automate the assembly of molded plastic packages with heat spreaders is provided in U.S. Patent No. 6,432,749 to Libres (the '749 patent), which is incorporated by reference herein in its entirety. The '749 patent describes a method of manufacture in which heat spreaders are provided in strip format to allow a plurality of packages to be assembled at the same time. The strip of heat spreaders is placed over the die and substrate, and a molding compound is disposed over the heat spreader, die, and substrate such that the edge of the encapsulating material is coincident with a reduced cross-sectional pillar connecting an adjacent heat spreader. The reduced cross-sectional pillar is then cut to separate the packages. Where the substrate is a leadframe, the '749 patent teaches adhering the heat spreader to the leadframe using a non-conductive adhesive to prevent electrical shorting. While this method provides for automated assembly, the assembly requires that the packages be individually molded (i.e., pocket molded) to reveal the cross-sectional pillar so that it may be cut. The use of individual molds for each package is undesirable for certain applications because it makes the molding process prone to manufacturing defects such as misalignment of the individual molds relative to the die and substrate. In addition, the method described in the '749 patent requires the use of non-conductive adhesive to secure the heat spreader to the leadframe to prevent electrical short circuits. This is another step in the manufacturing process where defects can occur.

There remains, therefore, a need for a method for the automated manufacture of thermal enhanced molded plastic packages that does not require complex manufacturing steps.

BRIEF SUMMARY OF THE INVENTION

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The above-described and other drawbacks and deficiencies of the prior art are overcome or alleviated by a device comprising: a substrate having first and second generally opposite surfaces, the substrate first surface having a plurality of bond sites disposed thereon; a die having first and second generally opposite surfaces parallel to the substrate first and second surfaces, the die first surface having a plurality of I/O pads disposed thereon, the I/O pads being electrically connected to the bond sites; a molding compound encapsulating the die and at least the first surface of the substrate; and a heat spreader at least partially embedded in the

molding compound. The heat spreader has a peripheral cut edge portion aligned with and not recessed from associated peripheral cut edge portions of the substrate and molding compound.

The substrate may be a metallic lead frame, or may comprise a dielectric material having first electrical conductors disposed thereon, with the first electrical conductors being selected from at least one of electrically conductive traces, layers, vias, pins, and combinations including one or more of the foregoing. An array of second electrical conductors may 5 electrically couple the substrate to an external circuit, with the second electrical conductors being selected from at least one of solder balls, solder bumps, solder paste, pins, and combinations including one or more of the foregoing. The I/O pads on the die may be 10 electrically connected to the bond sites on the substrate using wire bonding, tape bonding, or flip-chip methods.

In one embodiment, heat spreader has first and second generally opposite surfaces parallel to the die first and second surfaces. The heat spreader second surface is covered by the molding compound, and the heat spreader first surface is uncovered by the molding compound. 15 The heat spreader may include a protrusion extending from the heat spreader second surface, with the protrusion contacting the die. In another embodiment, the heat spreader includes a down-set leg contacting the substrate first surface. The heat spreader may be electrically connected to the substrate. Alternatively, the heat spreader is entirely separated from the substrate.

20 In another aspect of the invention, a method for manufacturing packaged semiconductor devices includes: disposing a plurality of dies onto a plurality of interconnected substrates; electrically connecting I/O pads on each die in the plurality of dies to bond sites on an associated substrate in the plurality of interconnected substrates; securing a plurality of interconnected heat spreaders over the plurality of dies; overmolding the plurality of dies, the bond sites, and the plurality of interconnected heat spreaders with a continuous coating of 25 molding compound to form a plurality of interconnected package precursors; and singulating the interconnected package precursors to provide a plurality of packages.

In one embodiment, the plurality of interconnected heat spreaders includes a downset portion disposed at a perimeter of the plurality of interconnected heat spreaders. Securing the 30 plurality of interconnected heat spreaders over the plurality of dies includes disposing the downset portion on the plurality of interconnected substrates, and the singulating includes singulating through the downset portion to provide a package having a heat spreader entirely separated from the substrate.

The details of one or more embodiments of the invention are set forth in the accompanying drawings and the description below. Other features, objects and advantages of the invention will be apparent from the description and drawings, and from the claims.

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BRIEF DESCRIPTION OF THE DRAWINGS

The invention will be more fully understood from the following detailed description taken in conjunction with the accompanying drawings wherein like elements are numbered alike, and in which:

10 FIG. 1 is a cross-sectional elevation view of an exemplary package strip of the present invention having an interposer substrate;

FIG. 2 is a cross-sectional elevation view of a package singulated from the strip of FIG. 1;

15 FIG. 3 is a cross-sectional elevation view of an alternative embodiment of a package strip having an interposer substrate;

FIG. 4 is a cross-sectional elevation view of a package singulated from the strip of FIG. 3;

FIG. 5 is a cross-sectional elevation view of an exemplary package strip of the present invention having a lead frame substrate;

20 FIG. 6 is a cross-sectional elevation view of a package singulated from the strip of FIG. 5;

FIG. 7a is a cross-sectional elevation view of an alternative embodiment of a package strip having a lead frame substrate;

25 FIG. 7b is a cross-sectional elevation view of the package strip of FIG. 7a having a thermally conductive material disposed between the dies and the heat spreader precursor;

FIG. 8a is a cross-sectional elevation view of a package singulated from the strip of FIG. 7a;

FIG. 8b is a cross-sectional elevation view of a package singulated from the strip of FIG. 7b;

30 FIG. 9 is a plan view of an array of interconnected heat spreaders forming a heat spreader precursor;

FIG. 10 is a cross-sectional elevation view of the array of interconnected heat spreaders taken along section A-A of FIG. 9;

FIG. 11 is a cross-sectional elevation view of the array of interconnected heat spreaders taken along section A-A of FIG. 9 including a die contact protrusion;

FIG. 12 is a plan view of an alternative embodiment of the array of interconnected heat spreaders forming a heat spreader precursor;

5 FIG. 13 is a cross-sectional elevation view of the array of interconnected heat spreaders taken along section B-B of FIG. 12;

FIG. 14 is a cross-sectional elevation view of the array of interconnected heat spreaders taken along section B-B of FIG. 12 including a die contact protrusion;

10 FIG. 15 is a plan view of an alternative embodiment of the array of interconnected heat spreaders forming a heat spreader precursor;

FIG. 16 is a cross-sectional elevation view of the array of interconnected heat spreaders taken along section C-C of FIG. 15;

FIG. 17 is a cross-sectional elevation view of the array of interconnected heat spreaders taken along section C-C of FIG. 15 including a die contact protrusion;

15 FIG. 18 is a plan view of a package strip of the present invention in various stages of manufacture;

FIG. 19 is a cross-sectional elevation view of the package strip taken along section D-D of FIG. 18; and

FIG. 20 is a flow chart depicting a method for manufacturing a molded plastic package.

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DETAILED DESCRIPTION

Referring to FIG. 1, a cross-sectional elevation view of a package strip, shown generally at 10, includes three interconnected package precursors 12, each of which includes a die 14 electrically connected to a substrate 16 and coated with a molding compound 18. 25 Disposed above the die 14 and partially encapsulated in the molding compound 18 of each package precursor 12 is a heat spreader 20. The substrates 16 are interconnected to form part of a substrate strip 22, and the heat spreaders 20 are interconnected to form part of a heat spreader precursor 24. The heat spreader precursor 24 includes down-set leg portions 26 that 30 are coupled to edges of the substrate strip 22.

In the manufacture of package strip 10, the heat spreader precursor 24 is placed over the dies 14 after the dies 14 have been electrically connected to the substrate strip 22 and before the molding compound 18 is applied to the heat spreader precursor 24, the dies 14, and a portion of the substrate strip 22. The molding compound 18 is advantageously applied to the

strip 10 using a block mold, where a continuous coating of molding compound 18 overmolds each package precursor 12 in the package strip 10. After the molding compound 18 is cured, the package strip 10 is then cut along lines 28, such as by blade saw, punch, laser, water jet, etc., to singulate the package precursors 12 into individual packages, one of which is shown 5 generally as 30 in FIG. 2.

As shown in FIG. 2, each singulated package 30 includes a die 14 electrically connected to a portion of the substrate strip 22 (FIG. 1), which forms the substrate 16 for the package 30, and a portion of the heat spreader precursor 24 (FIG. 1), which forms the heat spreader 20 for the package 30. The heat spreader 20 is at least partially embedded in the molding compound 10 18 and has a peripheral cut edge portion 32 aligned with and not recessed from associated peripheral cut edge portions 34 and 36 of the substrate 16 and molding compound 18, respectively. The heat spreader 20 enhances the thermal and advantageously electrical performance of the package 30. In addition, the heat spreader 20 shields the die 14 from electro-magnetic forces acting on the package 30.

15 Referring again to FIG. 1, the heat spreader precursor 24 may include reduced thickness notch areas 38 disposed at sides of the package precursors 12. The reduced thickness notch areas 38 ease singulation of the package precursors 12 by reducing the thickness of the heat spreader precursor 24 that must be cut during singulation. After singulation, the notch areas 38 form part of a shoulder that remains embedded in the molding compound 18, as seen 20 in FIG. 2. The molding compound 18 acts on the shoulder to anchor the heat spreader 20 within the molding compound 18.

The down-set legs 26 support each of the interconnected heat spreaders 20 above the dies 14 in the substrate strip 10. Singulation of the package precursors 12 removes the legs 26, and results in a package 30 having a heat spreader 20 that is entirely separated from the 25 substrate 16. Because the heat spreader 20 is entirely separated from the substrate 16, electrical isolation of the heat spreader 20 and the substrate 16 is ensured without the need for non-conductive adhesive, as is used with heat spreader designs of the prior art.

While in many package designs it is advantageous for the heat spreader 20 to be 30 electrically isolated from the substrate 16, in other applications it is desirable to provide an electrical connection between the heat spreader 20 and substrate 16. For example, it may be desired to electrically ground the heat spreader 20 by electrically attaching the heat spreader 20 to a bond site on the substrate 16 associated with electrical ground. For use in such applications, an alternative to the heat spreader precursor 24 may be used, as shown in FIG. 3.

In the embodiment of FIG. 3, a heat spreader precursor 24 includes down-set legs 52 positioned between each of the package precursors 12 in addition to the legs 26 disposed at the perimeter of the heat spreader precursor 24. As shown by lines 28, singulation of the package precursors 12 occurs at the portion of legs 52 contacting the substrate strip 22 and, as a result, 5 portions of the legs 52 remain attached to each substrate 16 after singulation.

FIG. 4 depicts a cross sectional view of one package 54 singulated from the package strip 10 of FIG. 3. As seen in FIG. 4, each of the legs 52 is adhered to a pad 56 formed on the substrate 16 to form an electrical connection therebetween. Preferably, the leg 52 is adhered to the pad 56 with an epoxy adhesive.

10 In the embodiments of FIGs. 1-4, electrical connection between each input/output (I/O) pad 60 on the die 14 and an associated bond site 62 on the substrate 16 is accomplished by wire bonding or tape bonding, wherein one end of a wire 64 or conductive tape (not shown) is connected to the I/O pad 60 and an opposite end of the wire 64 is connected to the bond site 62. It will be appreciated, however, that the electrical connection between the I/O pads 60 on 15 the die 14 and the bond sites 62 on the substrate 16 may alternatively be accomplished by a flip-chip type connection, wherein the die 14 is flipped such that its I/O pads 60 can be directly electrically connected to the bond sites 62. By "directly" electrically connected it is meant that the interconnection is by a flip chip method without the use of an intervening wire bond or tape bonding tape. Suitable attachments include solders with a primary constituent selected from 20 the group consisting of gold, tin, and lead.

25 In the embodiment of FIGs. 1-4, the substrates 16 are depicted as being of the interposer type of substrate, which comprises multiple layers, shown as 3 thin layers 66, of dielectric material having electrical conductors 72 (e.g., traces, pins, vias, and the like) formed thereon. The electrical conductors 72 terminate at lands 68, which are exposed at the surface of the package 30 or 34. Additional electrical conductors 70 such as solder balls, solder 30 bumps, solder paste, pins, and the like may be attached to the lands 68. Electrical signals are transmitted between the I/O pads 60 on the die 14 and external circuitry, such as a printed circuit board, by the wires 64, electrical conductors 72, lands 68, and electrical conductors 70. It will be appreciated that, while the present invention is applicable to molded plastic packages 35 having an interposer substrate, the present invention is equally applicable to molded plastic packages having other types of substrates. For example, FIGs. 5-8 depict molded plastic packages having lead frame type substrates.

Referring to FIG. 5, a cross-sectional elevation view of a package strip 10 having a metallic lead frame type substrate strip 22 includes three interconnected package precursors 12,

each of which includes a die 14 electrically connected to the substrate 16 and coated with the molding compound 18. The substrate strip 22 includes a plurality of interconnected substrates 16 in the form of lead frames. The lead frames type substrate 16 are formed from a metallic material, such as copper or copper alloy. Each substrate 16 includes a die pad 80, to which the 5 die 14 is adhered, and a plurality of leads 28 spaced apart from the die pad 80. Disposed above the die 14 and partially encapsulated in the molding compound 18 of each package precursor 12 is the heat spreader 20, which is interconnected with adjacent heat spreaders 20 to form the heat spreader precursor 24. The heat spreader precursor 24 used in the embodiment of FIG. 5 is substantially similar to the heat spreader precursor 24 described with reference to the 10 embodiment of FIGs. 1-4.

Each lead 82 has an inner lead end and an opposing outer lead end. Formed on inner lead end is the bond site 62, which is electrically connected to the I/O pad 60 on the die 14. The outer lead end provides the land 68 for connecting to external circuitry. The leads 82 may have reduced thickness notch areas 84 disposed therein to ease singulation of the package 15 precursors 12 by reducing the thickness of the substrate 16 that must be cut during singulation.

In the manufacture of the package strip 10 of FIG. 5, the heat spreader precursor 24 is placed over the dies 14 after the dies 14 have been attached to the die pads 80 and electrically connected to the leads 82 and before the molding compound 18 is applied. The molding compound 18 is advantageously applied to the strip 10 using a block mold. After the molding compound 18 is cured, the package strip 10 is then cut along lines 28, such as by blade saw, 20 punch, laser, water jet, etc., to singulate the package precursors 12 into individual packages, one of which is shown generally at 86 in FIG. 6.

As shown in FIG. 6, each singulated package 86 includes a portion of the heat spreader precursor 24 (Fig. 5), which forms the heat spreader 20 for the package 86. The heat spreader 25 20 is at least partially embedded in the molding compound 18 and has a peripheral cut edge portion 32 aligned with and not recessed from associated peripheral cut edge portions 34 and 36 of the substrate 16 and molding compound 18, respectively. As in the previously-discussed embodiments, the heat spreader 20 enhances the thermal and advantageously electrical 30 performance of the package 86. In addition, the heat spreader 20 shields the die 14 from electro-magnetic forces acting on the package 86.

FIGs. 7a and 7b are each a cross-sectional elevation view of a package strip 10 having a lead frame type substrate 16 wherein the dies 14 are electrically connected to the substrate in flip-chip fashion. The package strip 10 is substantially similar to that shown in FIG. 5, with the exception being that the lead frame substrate 16 of FIGs. 7a and 7b does not include a die pad,

and the leads 82 of FIGs. 7a and 7b extend beneath the die 14 for direct electrical connection with the I/O pads 60. As in the previous embodiments, the package strip 10 is cut along lines 28, such as by blade saw, punch, laser, water jet, etc., to singulate the package precursors 12 into individual packages. FIG. 7a depicts the package strip 10 wherein the dies 14 are 5 separated from the heat spreader precursor 24 by the molding compound 18. FIG. 7b is identical to FIG. 7a, with the exception being that in the package strip 10 of FIG. 7b, the heat spreader precursor 24 is attached to each of the dies by a layer of material 88 having a higher thermal conductivity than that of the molding compound 18. For example, the layer of material 88 may be a layer of thermally conductive paste or a layer of metallic material 10 disposed on the die 14.

As shown in FIGs. 8a and 8b, each singulated package 90 includes a die 14 directly electrically connected to the leads 82. Each singulated package 90 also includes a portion of the heat spreader precursor 24 (FIG. 7a and 7b), which forms the heat spreader 20 for the package 90. The heat spreader 20 is at least partially embedded in the molding compound 18 and has a peripheral cut edge portion 32 aligned with and not recessed from associated peripheral cut edge portions 34 and 36 of the substrate 16 and molding compound 18, respectively. As in the previously-discussed embodiments, the heat spreader 20 enhances the thermal and electrical performance of the package 90. In the embodiment of FIG. 8b, the layer of material 88 disposed between the die 14 and the heat spreader 20 further enhances the 15 thermal performance of the package 90 by increasing the thermal conductivity between the die 14 and the heat spreader 20.

Referring to FIGs. 9 and 10, the heat spreader precursor 24 for use in the embodiments of FIGs. 1, 2, and 5-8 is shown. The heat spreader precursor 24 includes a plurality of interconnected heat spreaders 20, each of which includes a planar top surface 92, an opposing 25 planar bottom surface 94, and a shoulder surface 96 extending around, and recessed from, the perimeter of the top surface 92. Each heat spreader 20 is interconnected to adjacent heat spreaders 20 by a pair of tie bars 98. As can be seen in the elevation view of FIG. 10, the tie bars 98 and shoulders 96 form the reduced thickness notch area 38. Disposed around the perimeter of the heat spreader precursor 24 are the legs 26. It will be appreciated that, 30 although it is not shown, the heat spreader precursor for the embodiment of FIGs. 3 and 4 may be obtained from the heat spreader precursor 24 by shaping the tie bars 98 into legs 52, as depicted in FIGs. 3 and 4.

The heat spreader precursor 24 may be formed from a sheet of any material having a thermal conductivity greater than that of the molding compound 18. Preferably, the heat

spreader precursor 24 is a metal such as, for example, copper, aluminum, or alloys containing one or more of copper and aluminum. Copper and copper base alloys are preferred, for example, where higher thermal conductivity is required, while aluminum or an aluminum base alloy is preferred where a lighter weight heat spreader is required. As defined herein, the term 5 "alloy" refers to a mixture of two or more metals, or of one or more metals with certain nonmetallic elements, and the term "base" used in association with a metal refers to an alloy that contains at least 50 wt% of the specified element. Portions of each heat spreader 20, especially the top surface 92, which is exposed at the top of the package, may be plated with a metal, such as nickel, to prevent oxidation. The heat spreader 20 may also be treated for anti- 10 tarnishing and/or aesthetic properties using any convenient method, such as, for example, providing a black oxide coating on its surface.

The sheet of conductive material forming the heat spreader precursor 24 preferably has a thickness of between about 0.2 millimeters (mm) to about 0.5 mm, and more preferably between about 0.2 mm to about 0.3 mm. The features of the heat spreader precursor 24, 15 including the tie bars 98 and legs 26, may be formed using any known method such as stamping, chemical etching, laser ablation, or the like. The legs 26 are preferably formed into down-set shape by bending. The reduced thickness notch areas 38 are preferably formed using a controlled subtractive process such as chemical etching or laser ablation. The reduced thickness notch areas 38 are preferably etched sufficiently to provide a shoulder 96 and tie bar 20 98 thickness of between about 25% to about 60% of the thickness of the heat spreader 20 (i.e., the thickness between the top and bottom surfaces 92, 94), and more preferably between about 50% to about 60% of this thickness. Thicknesses within this preferred range provide sufficient clearance in the reduced thickness notch area 38 to receive molding compound 18, which locks the heat spreader 20 in the package, and reduces a sufficient amount of the metal to ease 25 singulation.

Referring to FIG. 11, an alternative embodiment of the heat spreader precursor 24 is shown wherein each heat spreader 20 includes a protrusion 100 extending from the bottom surface 94. Each protrusion 100 contacts an associated die 14 when the heat spreader 20 is installed in the package, thus allowing heat from the die 14 to be directly conducted to the heat 30 spreader 20. The heat spreader may be attached to the die using epoxy, adhesive, or the like. It will be appreciated that the heat spreader having the protrusion may be used in any of the embodiments of FIGs. 1-8.

Referring to FIGs. 12 and 13, another alternative embodiment of the heat spreader precursor 24 is shown. In the embodiment of FIG. 12, the heat spreader precursor 24 includes

a plurality of interconnected heat spreaders 20, each of which includes a planar top surface 92, an opposing planar bottom surface 94, and a shoulder surface 96 extending around, and recessed from, the perimeter of the top surface 92. Each heat spreader 20 is interconnected to adjacent heat spreaders 20 by a recessed planar portion 102. As can be seen in the elevation view of FIG. 13, the recessed planar portion 102 forms the portion of the reduced thickness notch area 38 that is cut during singulation. Disposed around the perimeter of the heat spreader precursor 24 are the legs 26, and disposed through portions of the legs 26 is a plurality of through holes 104, which allow the ingress of molding compound 18.

The sheet of conductive material forming the heat spreader precursor 24 of FIGs. 12 and 13 preferably has a thickness of between about 0.2 mm to about 0.5 mm, and more preferably between about 0.2 mm to about 0.3 mm. The features of the heat spreader precursor 24, such as the legs 26 and through holes 104, may be formed using any known method such as stamping, chemical etching, laser ablation, or the like. The legs 26 may be formed into shape by bending. The shoulder areas 26 are preferably etched to provide a thickness of between about 25% to about 60% of the thickness of the heat spreader 20 (i.e., the thickness between the top and bottom surfaces 92, 94), and more preferably between about 50% to about 60% of this thickness. Thicknesses within this preferred range provide sufficient clearance in the reduced thickness notch area 38 to receive molding compound 18, which locks the heat spreader 20 in the package, and reduces a sufficient amount of the metal to ease singulation.

20 The heat spreader precursor 24 of FIGs 12 and 13 may also include protrusions 100 for contacting the dies 14, as shown in FIG. 14.

Referring to FIGs. 15 and 16, another alternative embodiment of the heat spreader precursor 24 is shown. In the embodiment of FIG. 15, the heat spreader precursor 24 includes a plurality of interconnected heat spreaders 20, each of which includes a planar top surface 92, an opposing planar bottom surface 94, and a shoulder surface 96 extending around, and recessed from, the perimeter of the top surface 92. Each heat spreader 20 is interconnected to adjacent heat spreaders 20 by a recessed planar portion 102. As can be seen in the elevation view of FIG. 16, the embodiment of FIGs. 15 and 16 corresponds to the heat spreader precursor 24 of FIG. 3, wherein the heat spreader precursor 24 includes down-set legs 52 positioned between each of the package precursors 12 in addition to the legs 26 disposed at the perimeter of the heat spreader precursor 24. The recessed planar portion 102 forms the bottom portions of the legs 52, which are cut during singulation. Disposed through portions of the legs 26 and 52 is a plurality of through holes 104, which allow the ingress of molding compound 18. The number and position of the through holes 104 may be changed from that depicted in FIGs. 15

and 16 to provide different patterns of legs 26 and 52, as may be necessary if the heat spreader 20 is to be electrically connected to the substrate 16, as described with reference to FIGs. 3 and 4.

The sheet of conductive material forming the heat spreader precursor 24 of FIGs. 15 and 16 preferably has a thickness of between about 0.2 mm to about 0.5 mm, and more preferably between about 0.2 mm to about 0.3 mm. The features of the heat spreader precursor 24, such as the through holes 104, may be formed using any known method such as stamping, chemical etching, laser ablation, or the like. The legs 26 and 52 may be formed into shape by bending. The shoulder areas 26 are preferably etched to provide a thickness of between about 10 25% to about 60% of the thickness of the heat spreader 20 (i.e., the thickness between the top and bottom surfaces 92, 94), and more preferably between about 50% to about 60% of this thickness. The heat spreader precursor 24 of FIGs 15 and 16 may also include protrusions 100 for contacting the dies 14, as shown in FIG. 17.

In each of the embodiments described above, the package strip 10 and its associated 15 heat spreader precursor 24 and substrate strip 22 are depicted as having an array or matrix of three interconnected package precursors 12. It will be appreciated, however, that the present invention is applicable to any package strip 10 having two or more interconnected package precursors 12 are assembled in array or matrix fashion. For example, FIGs. 18 and 19 depict a package strip 10 configured for assembly 2x2 matrix of interconnected package precursors.

20 FIG. 18 is a plan view of an example of a package strip 10 of the present invention in various stages of manufacture. FIG. 19 is a cross-sectional elevation view of the package strip 10 of FIG. 18, and FIG. 20 is a flow chart depicting a method 118 for manufacturing a molded plastic package, which may be used to manufacture any of the embodiments described herein. In the method 118, each die 14 is secured to the substrate strip 22 using any convenient 25 method, such as solder, epoxy, double sided adhesive tape, and the like (block 120 of method 118). After the dies 14 are secured to the substrate strip 22, wires 64 are individually connected between the I/O pads 60 on the dies 14 and the bond sites 62 on the substrate strip 22 (block 122). Alternatively, where the I/O pads 60 are to be electrically connected to the bond sites 62 using a flip-chip method, steps 120 and 122 are performed simultaneously, with 30 the electrical connection between each die 14 and the substrate strip 22 also serving to mechanically attach the die 14 on the substrate.

After the dies 14 have been electrically connected to the substrate strip 22, thermally conductive material 88 (FIG. 7b) may be applied to the back of the dies if it is desired to have the heat spreaders 20 connected to the die 14 in each package (block 124). The heat spreader

precursor 24 is then disposed over the dies 14 with each heat spreader 20 in the heat spreader precursor 24 aligned above its respective die 14, and the legs 26 on the heat spreader precursor 24 are coupled to the substrate strip 22 using solder, epoxy, double sided adhesive tape, and the like (block 126).

5 With the heat spreader precursor 24 in position, the package precursors 12 are coated with molding compound 18 using a block molding method (block 128). That is, a continuous coating of molding compound 18 overmolds each package precursor 12 in the matrix of the package precursors 12. As shown in FIG. 18, the top surface 92 of each heat spreader 20 remains exposed from the molding compound 18. After the molding compound 18 has been 10 cured, the array of electrical conductors 70 (e.g., solder balls, solder bumps, solder paste, pins, etc.) may be attached to the substrate strip (block 130). The package precursors 12 are then singulated along lines 28 using any convenient method, such as, for example, saw, punch, laser, or water jet, to provide individual packages (block 132). It will be appreciated that method 118 may include various inspection steps, curing steps, cleaning steps, or other steps as may be 15 necessary for a particular package design.

Method 118 provides for the automated manufacture of thermal enhanced molded plastic packages while eliminating some of the complex manufacturing steps associated with methods of the prior art. For example, method 118 allows the use of a block molding technique for molding the packages in the package strip. As a result, the present invention 20 eliminates the problems associated with individual molding techniques used in the manufacture of thermally enhanced molded plastic packages of the prior art. In addition, because the heat spreader precursor 24 of the present invention does not need to be attached to the substrate within each individual package, the present invention eliminates the need to secure the heat spreader to the substrates within the individual package precursors using a dielectric adhesive, 25 as is necessary in methods of the prior art.

A number of embodiments of the present invention have been described. Nevertheless, it will be understood that various modifications may be made without departing from the spirit and scope of the invention. Accordingly, other embodiments are within the scope of the following claims.

WHAT IS CLAIMED IS:

1. A device comprising:

a substrate (16) having first and second generally opposite surfaces, the substrate (16) first surface having a plurality of bond sites (62) disposed thereon;
5 a die (14) mounted to the first side of the substrate (16), the die (14) having first and second generally opposite surfaces parallel to the substrate (16) first and second surfaces, the die (14) first surface having a plurality of I/O pads (60) disposed thereon, the I/O pads (60) being electrically connected to the bond sites (62);

10 a molding compound (18) encapsulating the die (14) and at least the first surface of the substrate (16);

a heat spreader (20) at least partially embedded in the molding compound (18) and having a peripheral cut edge portion (32) aligned with and not recessed from associated peripheral cut edge portions (34, 36) of the substrate (16) and molding compound (18).

15

2. The device of claim 1, wherein the heat spreader (20) has first and second generally opposite surfaces parallel to the die (14) first and second surfaces, the heat spreader (20) second surface being covered by the molding compound (18) and the heat spreader (20) first surface being uncovered by the molding compound (18).

20

3. The device of claim 2, wherein the heat spreader (20) is thermally connected to the die by a material (88) having a thermal conductivity higher than a thermal conductivity of the molding compound (18).

25

4. The device of claim 2, wherein the heat spreader (20) includes a protrusion (100) extending from the heat spreader (20) second surface, the protrusion contacting the die (14).

30 5. The device of claim 1, wherein the heat spreader (20) includes a down-set leg (52) contacting the substrate (16) first surface.

6. The device of claim 5, wherein the heat spreader (20) is electrically connected to the substrate (16).

7. The device of claim 1, wherein the heat spreader (20) is entirely separated from the substrate (16).

8. The device of claim 1, wherein the heat spreader (20) includes through holes (104) disposed therein for the ingress of the molding compound (18) between the heat spreader (20) and the substrate (16).

10

9. The device of claim 1, wherein the substrate (16) is a metallic lead frame.

10. The device of claim 1, wherein the substrate (16) comprises a dielectric material (66) having first electrical conductors (72) disposed thereon, the first electrical conductors (72) being selected from at least one of electrically conductive traces, layers, vias, pins, and combinations including one or more of the foregoing.

11. The device of claim 10 further comprising:
an array of second electrical conductors (70) electrically coupling the substrate (16) to an external circuit, the second electrical conductors (70) being selected from at least one of solder balls, solder bumps, solder paste, pins, and combinations including one or more of the foregoing.

12. The device of claim 1, further comprising:
a plurality of wires (64) or conductive tape strips, each being electrically connected between an I/O pad (60) on the die (14) first surface and a bond site (62) on the substrate (16) first surface.

13. The device of claim 1, wherein each of the I/O pads (60) on the die (14) is directly electrically connected to a bond site (62) on the substrate (16) in flip-chip fashion.

14. A method for manufacturing packaged semiconductor devices, the method comprising:

disposing a plurality of dies (14) onto a plurality of interconnected substrates (16);
electrically connecting I/O pads (60) on each die (14) in the plurality of dies to

5 bond sites (62) on an associated substrate (16) in the plurality of interconnected substrates;
securing a plurality of interconnected heat spreaders (20) over the plurality of dies (14);

10 overmolding the plurality of dies (14), the bond sites (62), and the plurality of interconnected heat spreaders (20) with a continuous coating of molding compound (18) to form a plurality of interconnected package precursors (12); and

singulating the interconnected package precursors (12) to provide a plurality of packages.

15. The method of claim 14, wherein the heat spreader (20) has first and

second generally opposite surfaces parallel to the die (14) first and second surfaces, and
wherein the overmolding results in the heat spreader (20) second surface being covered by
the molding compound (18) and the heat spreader (20) first surface being uncovered by
the molding compound (18).

20. 16. The method of claim 15, further comprising:

applying a thermally conductive material (88) to the back of the plurality of dies
after the electrically connecting and before the overmolding, the thermally conductive
material (88) having a thermal conductivity higher than the thermal conductivity of the
molding compound (18).

25

17. The method of claim 15, wherein the heat spreader (20) includes a
protrusion (100) extending from the heat spreader (20) second surface, the protrusion
contacting the die (14).

30. 18. The method of claim 14, wherein the plurality of interconnected heat
spreaders (20) include a down set portion disposed at a perimeter of the plurality of
interconnected heat spreaders, and wherein securing the plurality of interconnected heat

spreaders over the plurality of dies includes disposing the down set portion on the plurality of interconnected substrates, and the singulating includes singulating through the down set portion to provide a package (30) having a heat spreader (20) entirely separated from the substrate (16).

5 19. The method of claim 14, further comprising:

electrically connecting each heat spreader (20) in the plurality of heat spreaders to an associated substrate (16) in the plurality of substrates.

10 20. The method of claim 14, wherein the heat spreader (20) includes apertures disposed therein for the ingress of the molding compound (18) between the heat spreader (20) and the substrate (16).

21. The method of claim 14, wherein the substrate (16) is a metallic lead frame.

15 22. The method of claim 14, wherein the substrate (16) comprises a dielectric material (66) having first electrical conductors (72) disposed thereon, the first electrical conductors (72) being selected from at least one of electrically conductive traces, layers, vias, pins, and combinations including one or more of the foregoing.

20 23. The method of claim 22, further comprising:

electrically coupling an array of second electrical conductors (70) to the substrate (16), the second electrical conductors (70) being selected from at least one of solder balls, solder bumps, solder paste, pins, and combinations including one or more of the foregoing.

25

24. The method of claim 14, wherein the electrically connecting the I/O pads (60) to the bond sites (62) includes:

wire bonding or tape bonding the I/O pads (60) to the bond sites (62).

25. The method of claim 14, wherein the electrically connecting the I/O pads (60) to the bond sites (62) includes:

directly electrically connecting the I/O pads (60) to the bond sites (62) in flip-chip fashion.

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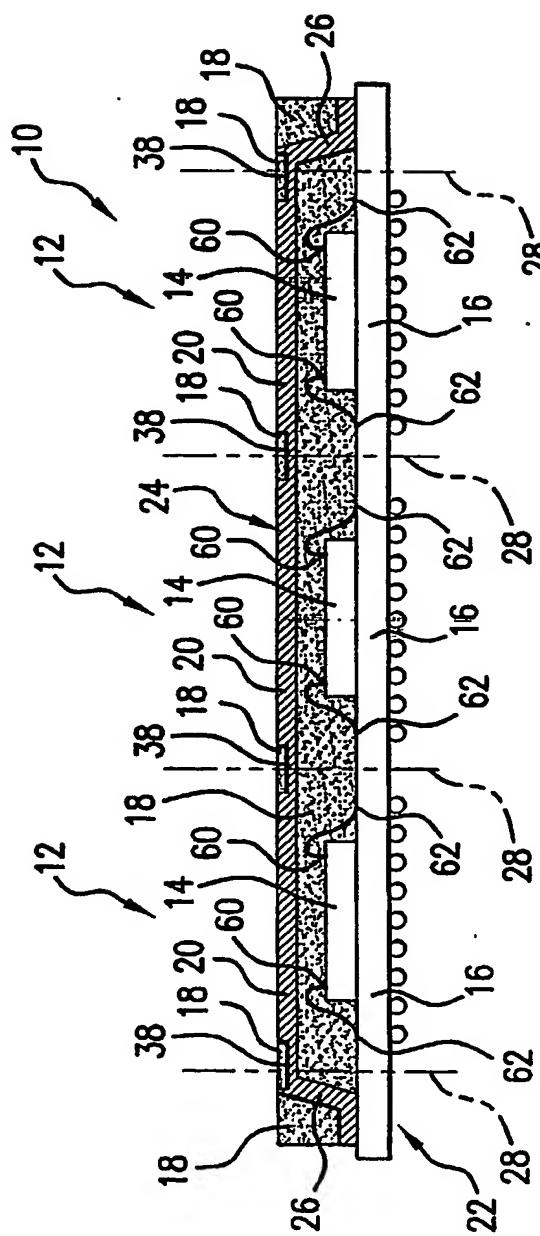


FIG. 1

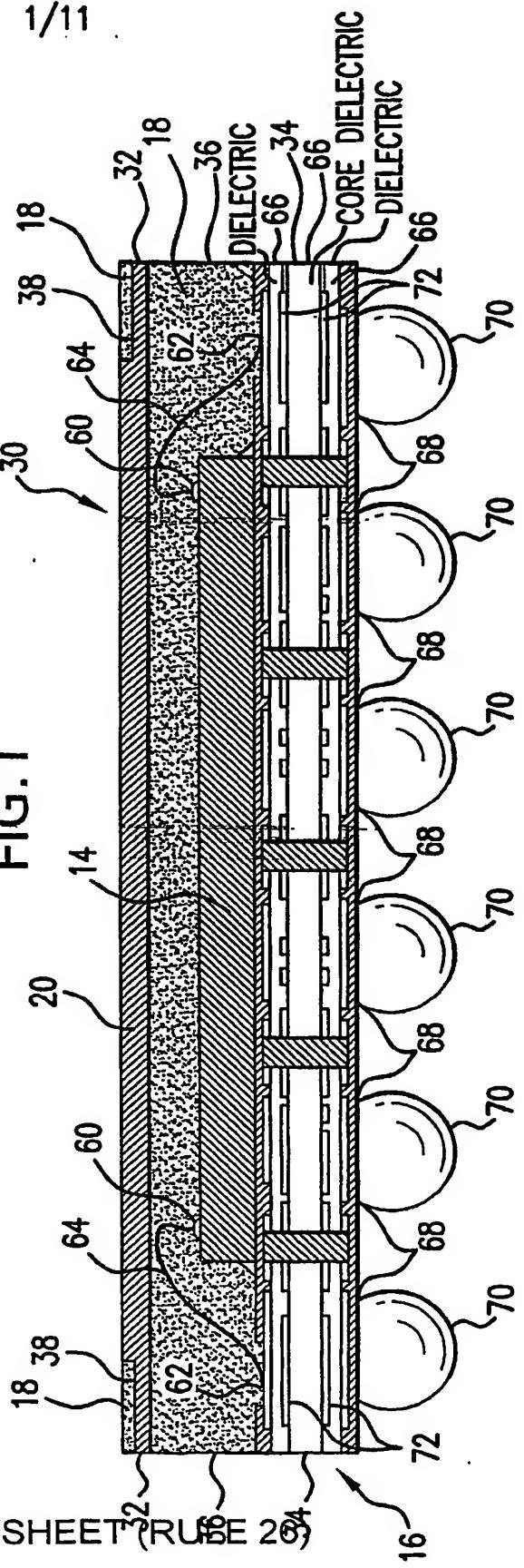
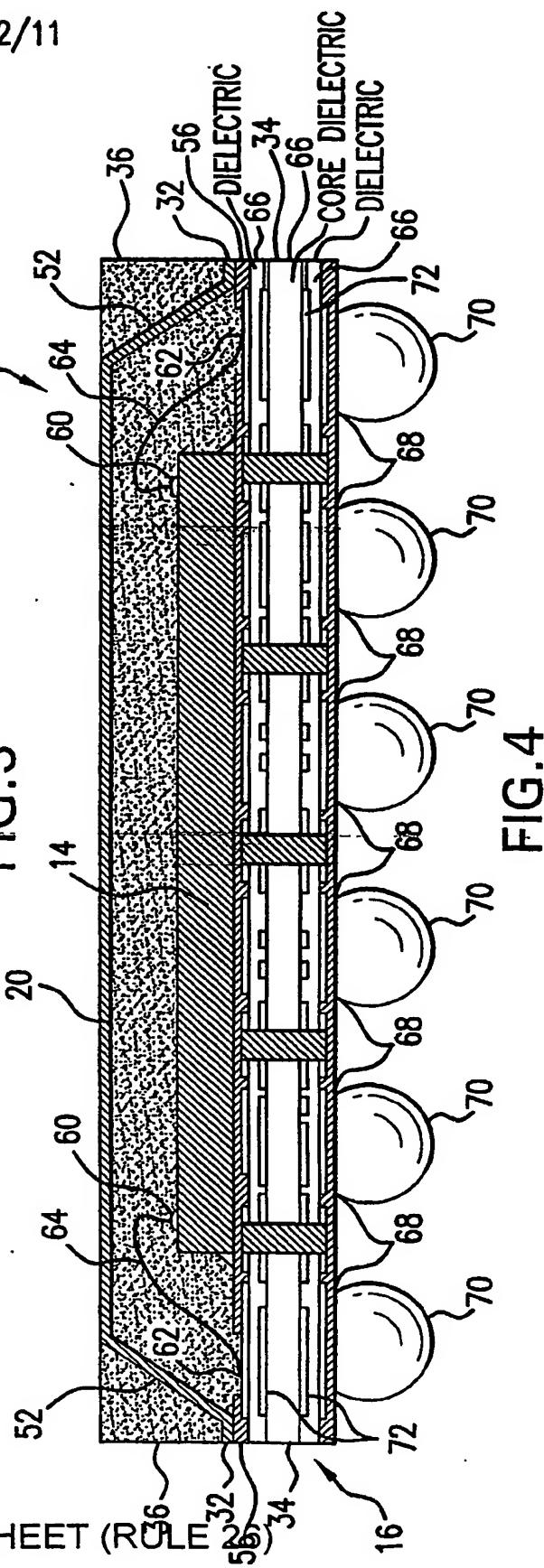
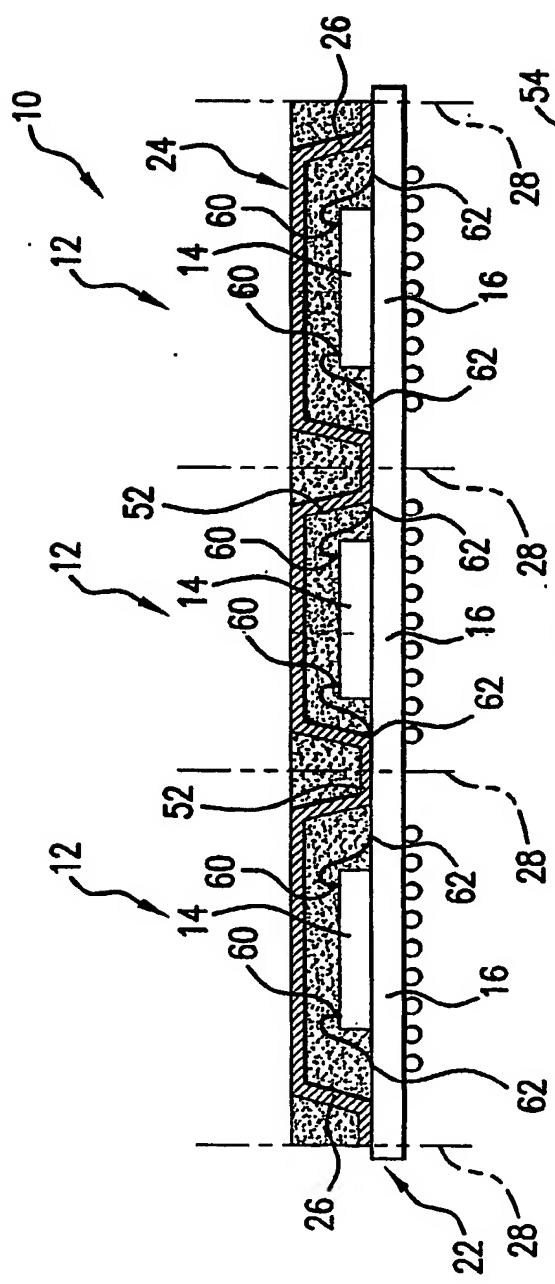


FIG. 2

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SUBSTITUTE SHEET (R56 L32 E26 56 34)

3/11

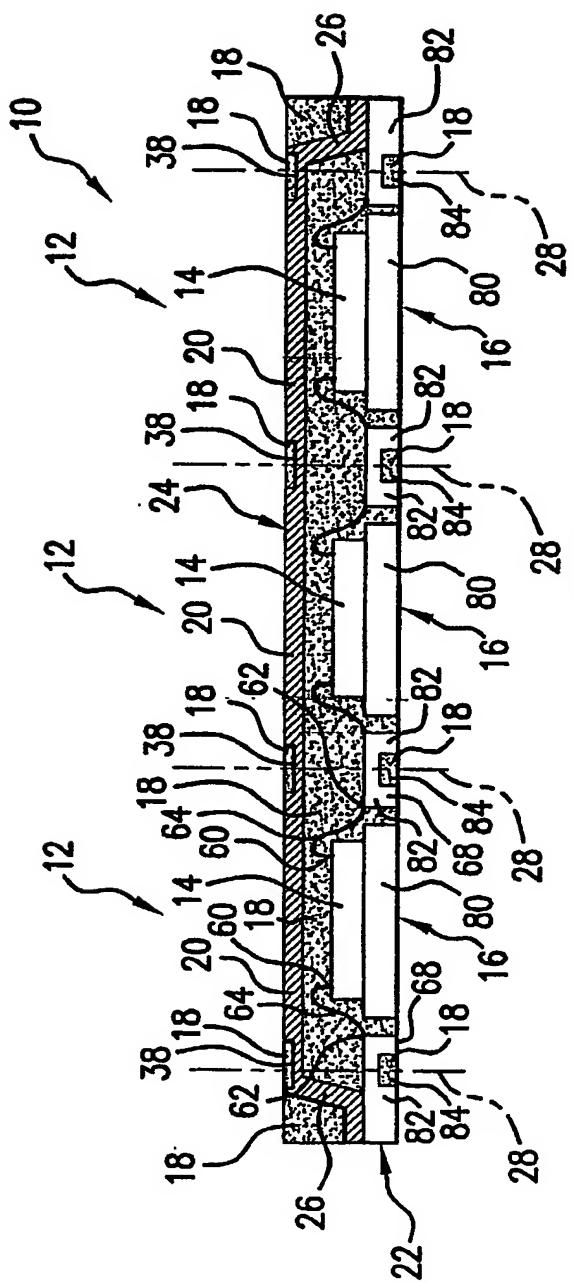
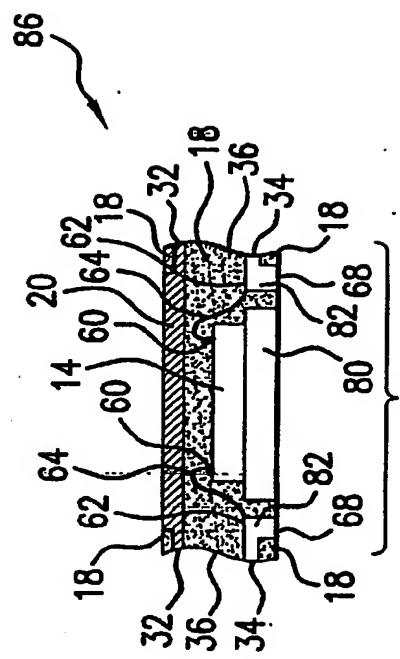


FIG. 5



6
FIG

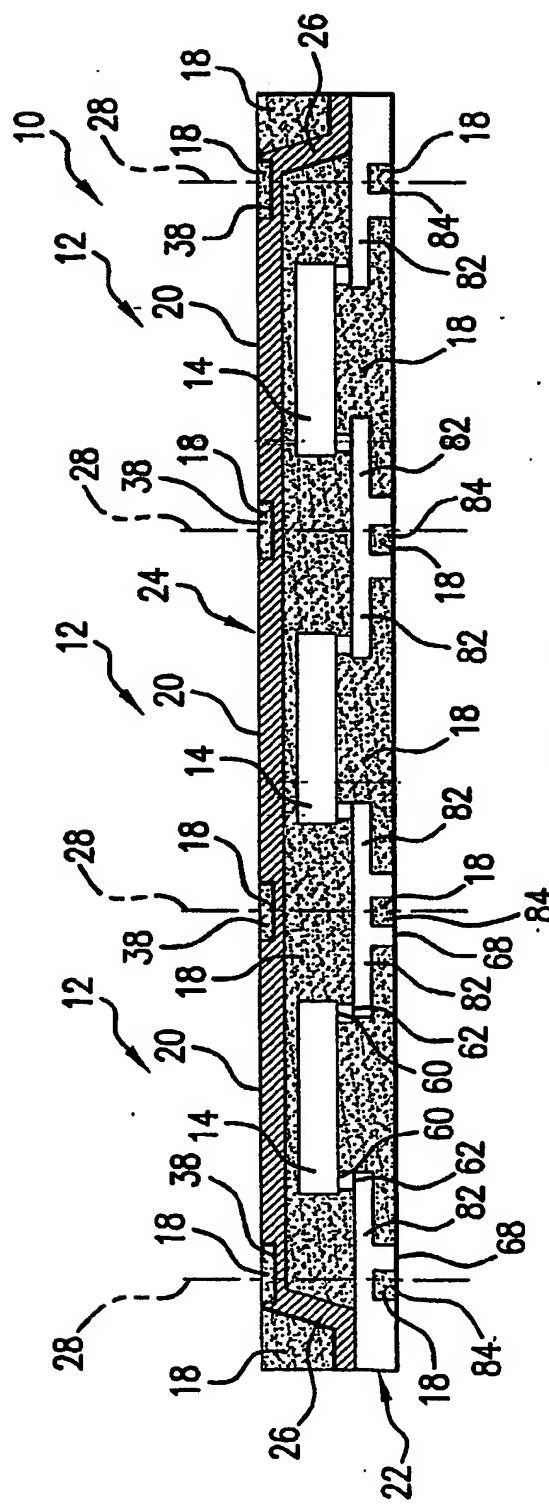


FIG. 7a

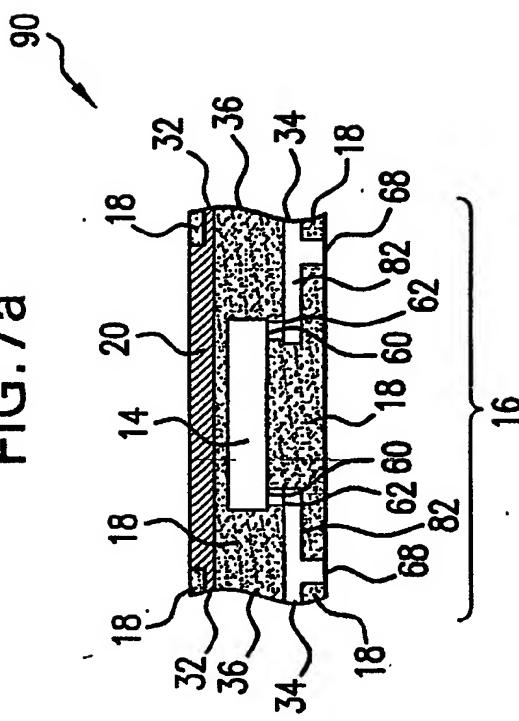
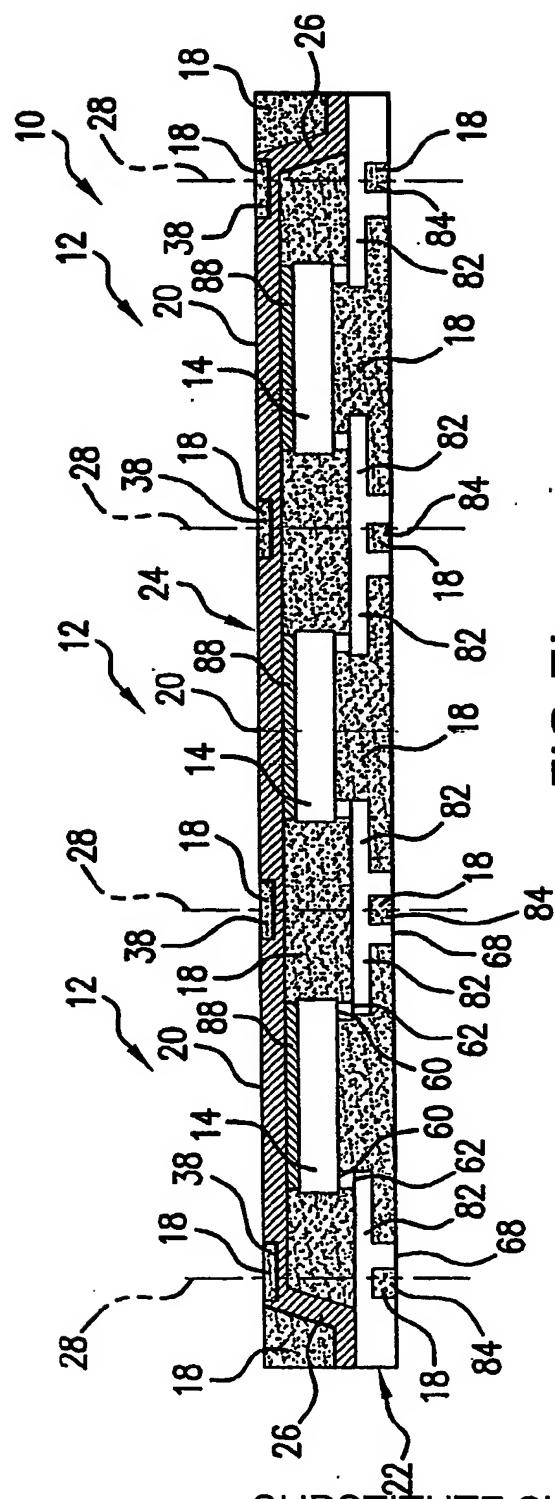


FIG. 8a



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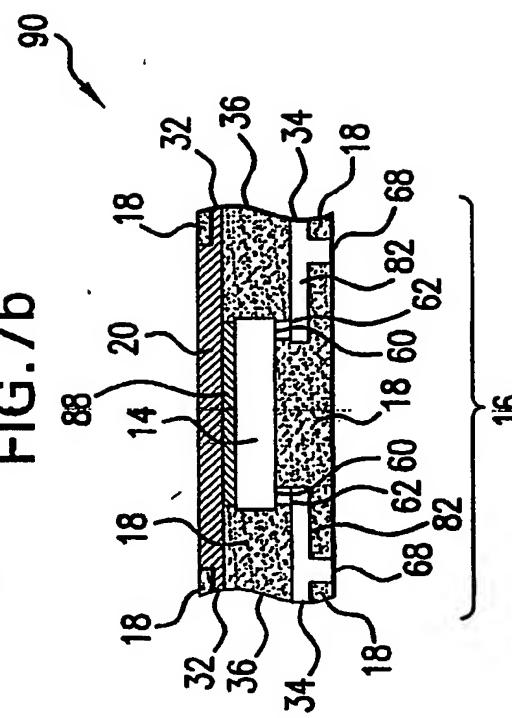


FIG. 8b

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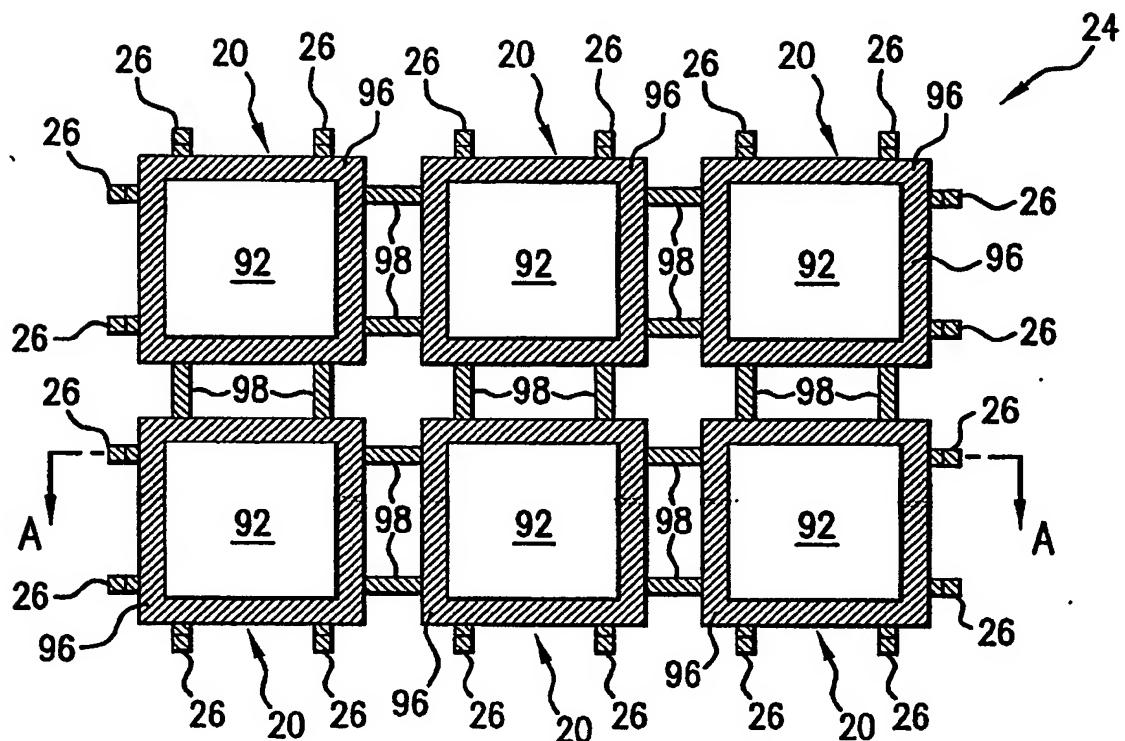


FIG. 9

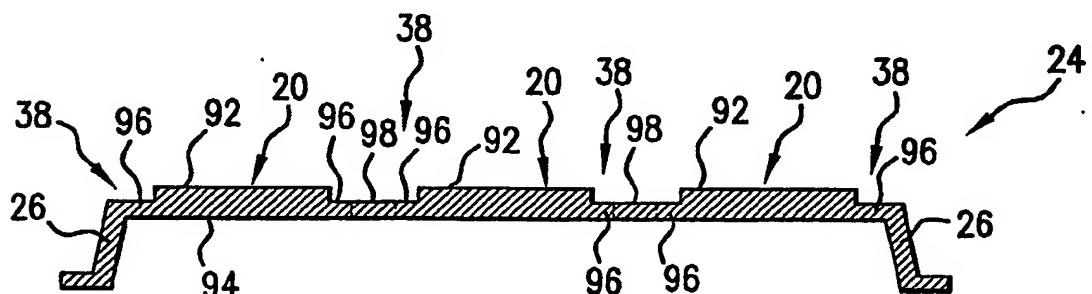
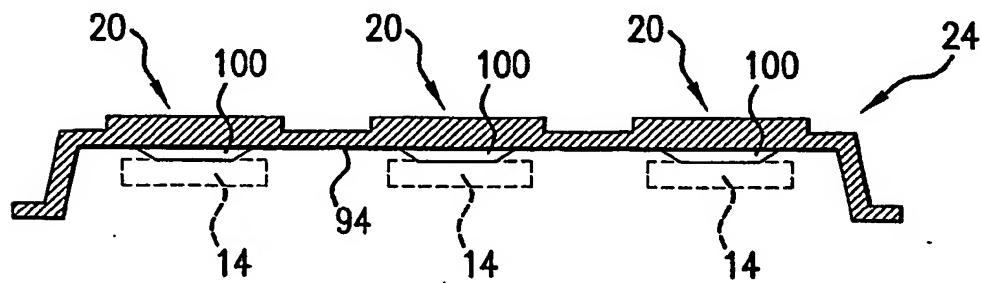


FIG. 10



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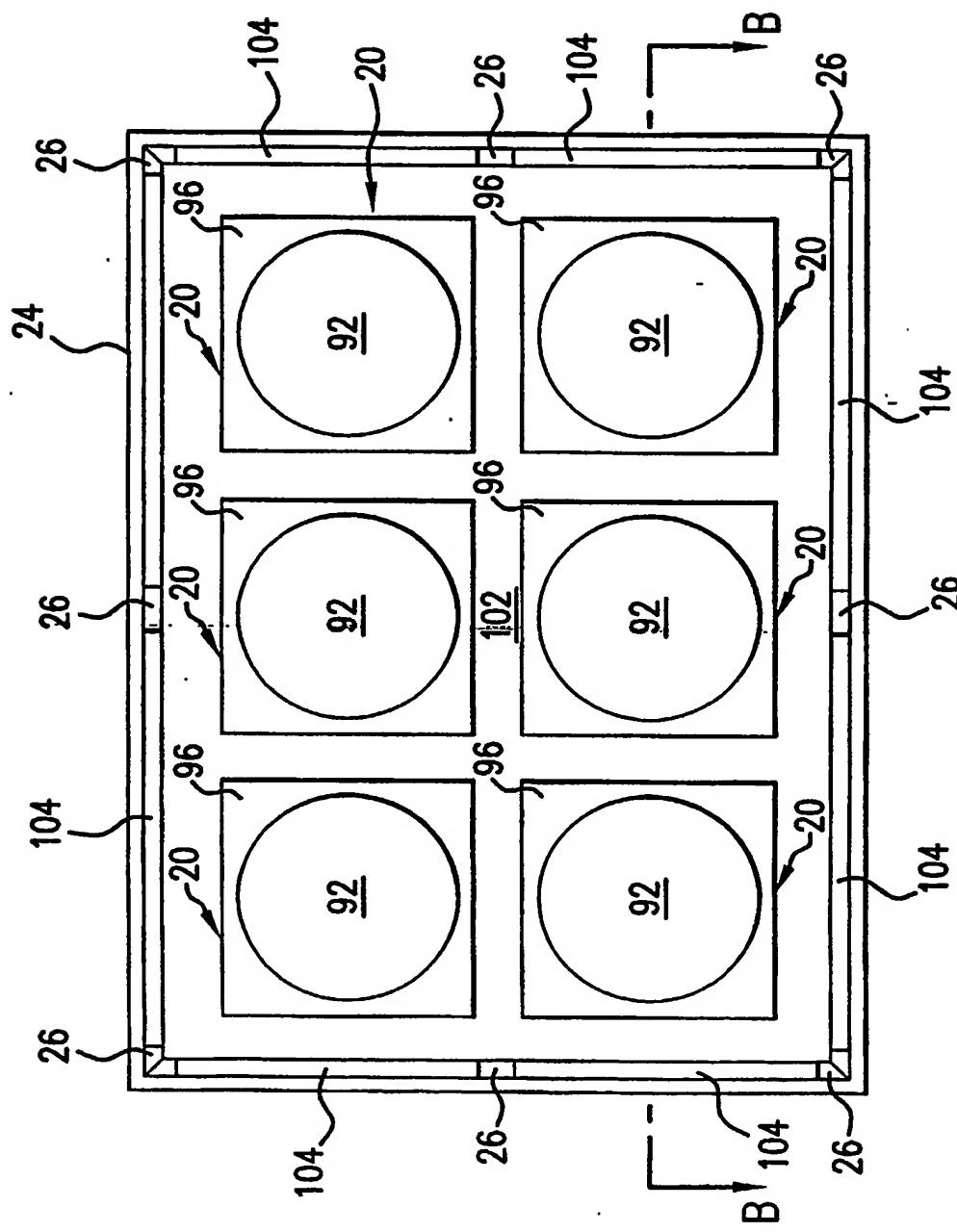


FIG. 12

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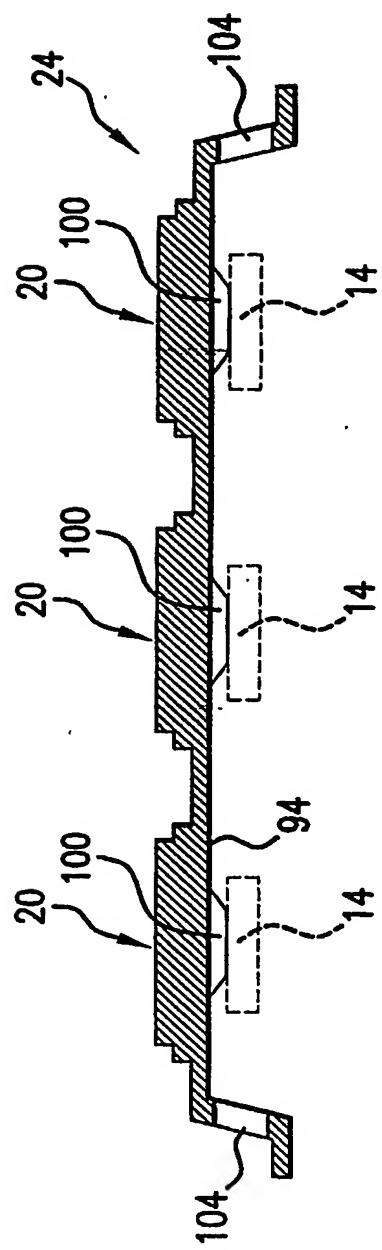
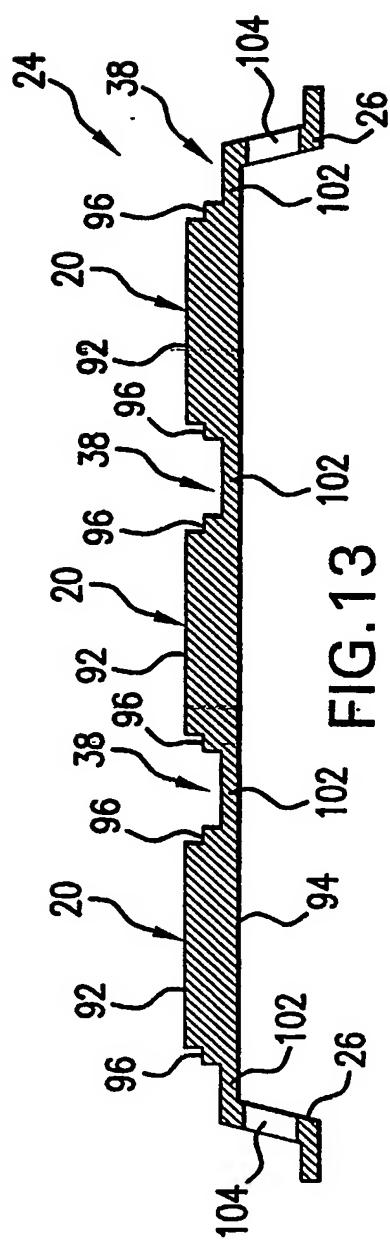


FIG. 14

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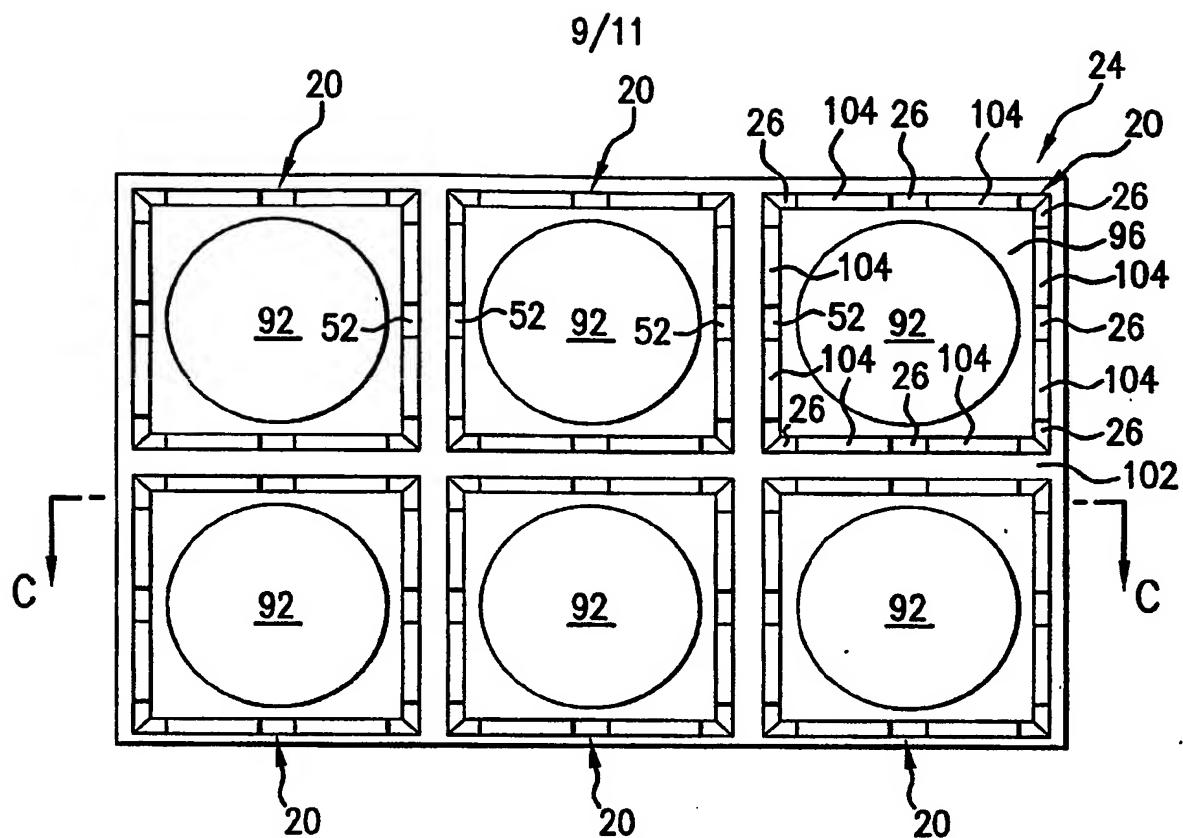


FIG. 15

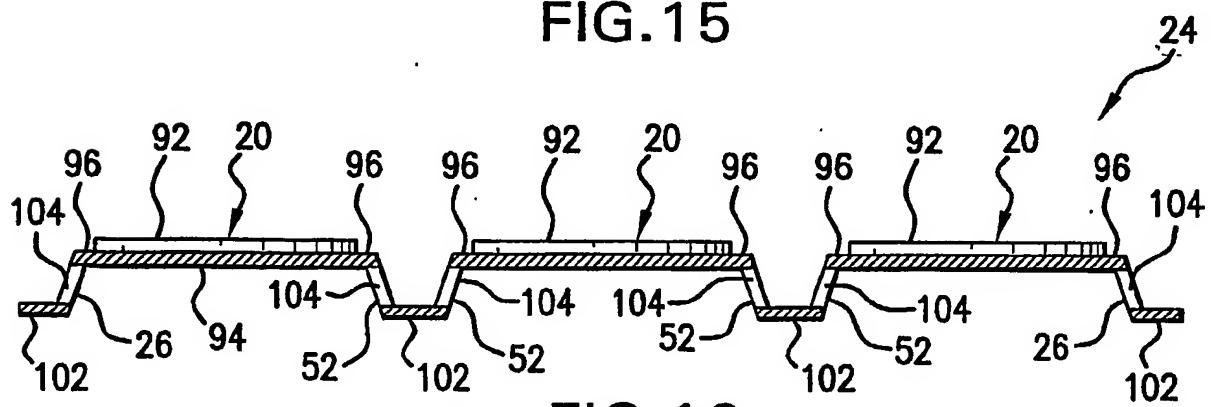
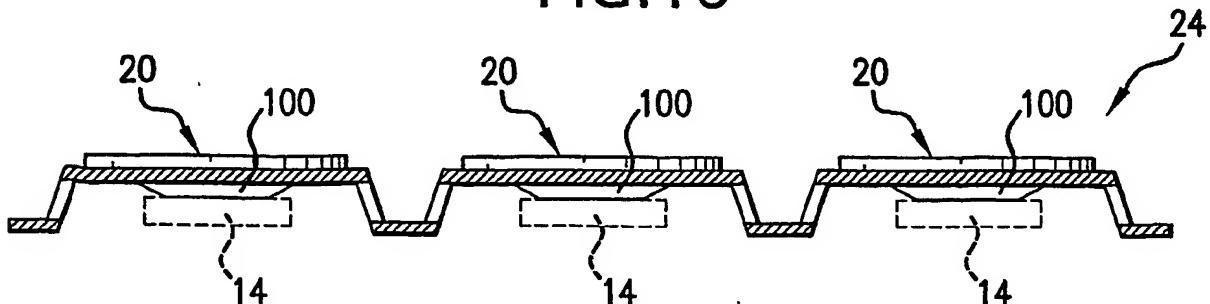


FIG. 16

FIG. 17
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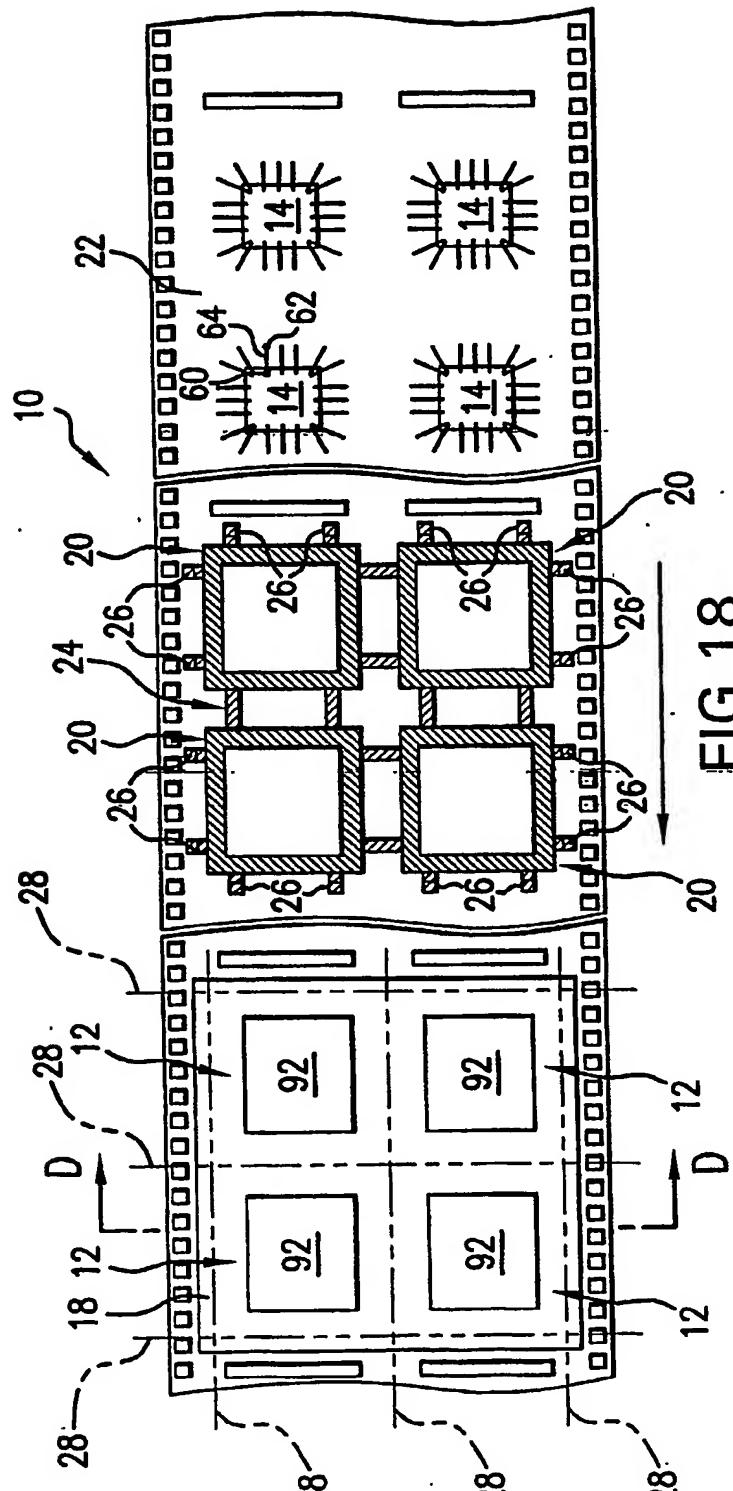


FIG. 18.

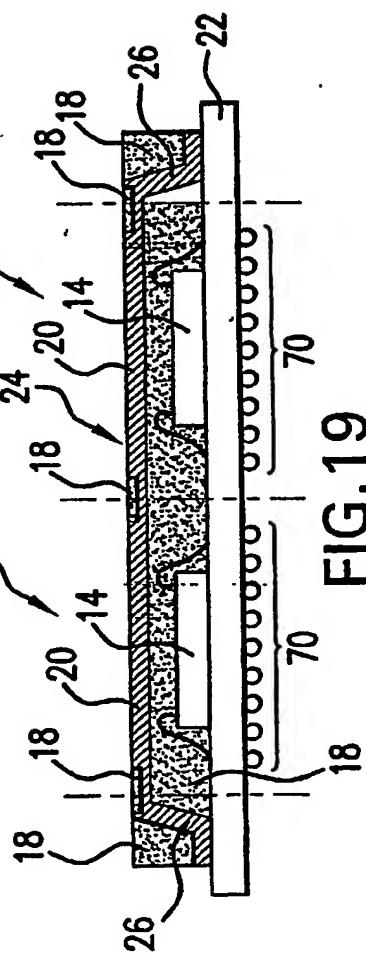
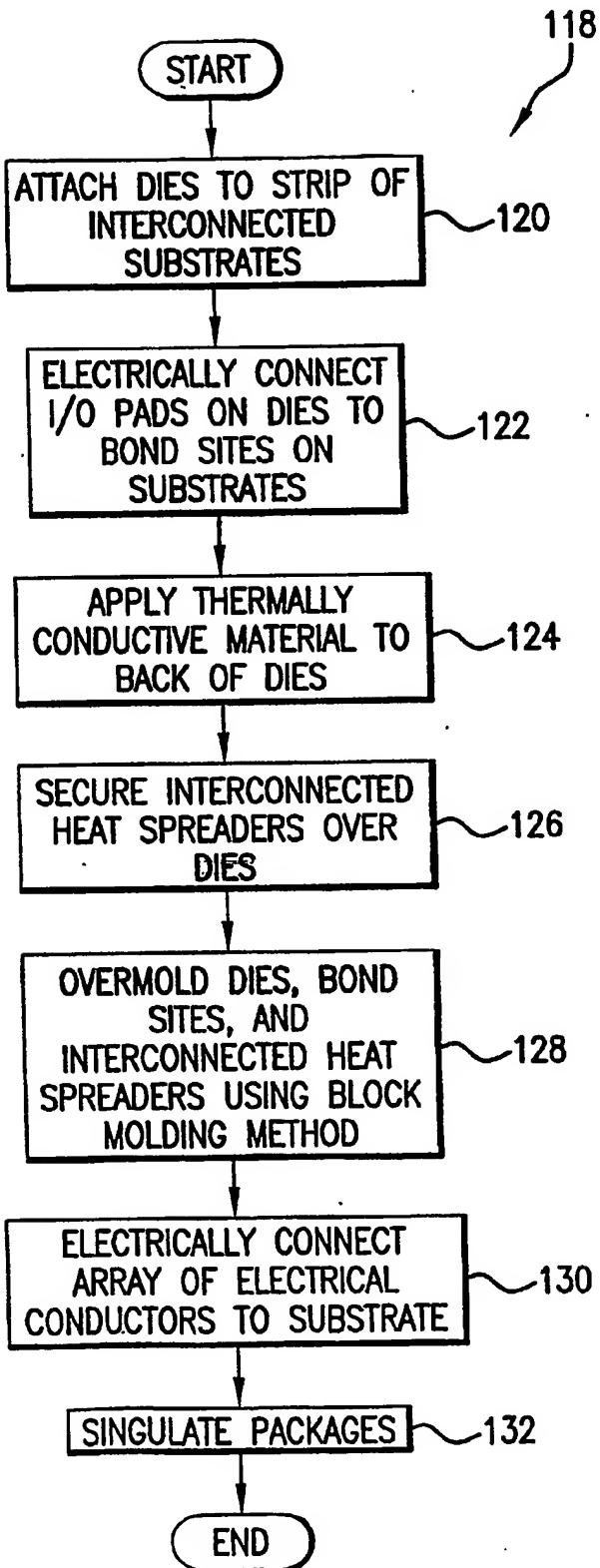


FIG. 19

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SUBSTITUTE SHEET 2 (RULE 26)

24 MAR 2005

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International Bureau**



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(71) **Applicant (for all designated States except US): ADVANCED INTERCONNECT TECHNOLOGIES LIMITED [MU/MU]; c/o Valmet (Mauritius) Limited, 608 St. James Court, St. Denis Street, Port Louis (MU).**

Published:

- with international search report
- before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments

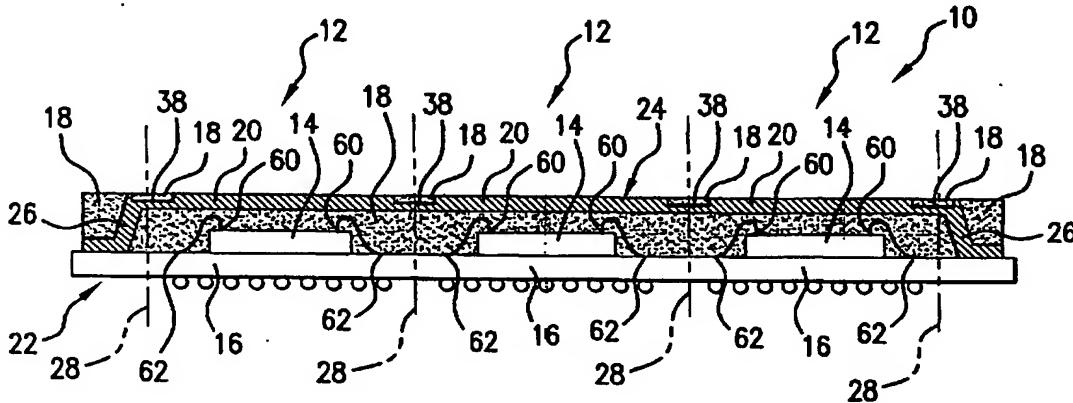
(72) Inventors; and

(75) Inventors/Applicants (for US only): LAU, Daniel, K. [US/US]; 395 Stonecrest Drive, San Francisco, CA 94312 (US). LAW, Edward, L., T. [US/US]; 925 Sunnyside Lane, Tracy, CA 95376 (US).

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(88) Date of publication of the international search report:
28 October 2004

(54) Title: THERMAL ENHANCED PACKAGE FOR BLOCK MOLD ASSEMBLY



WO 2004/032186 A3

(57) Abstract: A heat spreader (20) is added to a package to enhance thermal and advantageously electrical performance. In manufacture, a heat spreader precursor (24) is advantageously placed over a group of dies and secured after bonding (e.g., wire or tape bonding or flip-chip bonding) and before matrix/block mold. For example, a package strip (10) may consist of a row (linear array) of groups of die attach areas (e.g. in a rectangular array of four). The heat spreader precursor (20) may accommodate one such group or multiple groups along the package strip (10). The package strip (10) may then be singulated to form the individual packages. Each singulated package includes a die (14), its associated substrate 16 (e.g., either a lead frame or interposer type substrate) and a portion of the heat spreader precursor (24) as a heat spreader (20).

INTERNATIONAL SEARCH REPORT

International application No.

PCT/US03/29569

A. CLASSIFICATION OF SUBJECT MATTER

IPC(7) : H01L 23/02, 21/44, 21/48, 21/50
US CL : 257/660-678

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 257/660-678, 705-707, 713, 720, 734-784, 787, 796; 438/51, 55, 64, 106, 112, 113, 122, 123, 127

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched
Please See Continuation Sheet

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)
EAST

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	US 5,608,267 A (MAHULIKAR et al) 04 March 1997 (04.03.1997), see entire document.	1-25
Y	US 5,970,310 A (BANKS et al) 19 October 1999 (19.10.1999), see entire document.	1-25
X	US 6,432,742 B1 (GUAN et al) 13 August 2002 (13.08.2002), see entire document.	1-25
X	US 6,432,749 B1 (LIBRE) 13 August 2002 (13.08.2002), see entire document.	1-25
X	US 5,367,196 A (MAHULIKAR et al) 22 November 1994 (22.11.1994), see entire document.	1-25
X	US 6,432,752 A (FARNWORTH) 13 August 2002 (13.08.2002), see entire document.	1-25
X	US 5,650,663 A (PARTHASARATHI) 22 July 1997 (22.07.1997), see entire document.	1-25
A	US 5,919,329 A (BANKS et al) 06 July 1999 (06.07.1999), see entire document.	1-25
A	US 5,905,632 A (SETO et al) 18 May 1999 (18.05.1999), see entire document.	1-25

Further documents are listed in the continuation of Box C.

See patent family annex.

* Special categories of cited documents:	"T"	later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
"A" document defining the general state of the art which is not considered to be of particular relevance	"X"	document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
"E" earlier application or patent published on or after the international filing date	"Y"	document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	"&"	document member of the same patent family
"O" document referring to an oral disclosure, use, exhibition or other means		
"P" document published prior to the international filing date but later than the priority date claimed		

Date of the actual completion of the international search

04 January 2004 (04.01.2004)

Date of mailing of the international search report

20 AUG 2004

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Reshubha

INTERNATIONAL SEARCH REPORT

C. (Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 5,977,626 A (WANG et al) 02 November 1999 (02.11.1999), see entire document.	1-25
A	US 6,057,601 A (LAU et al) 02 May 2000 (02.05.2000), see entire document.	1-25
A	US 6,359,341 B1 (HUANG et al) 19 March 2002 (19.03.2002), see entire document.	1-25
A	US 6,409,859 B1 (CHUNG) 25 June 2002 (25.06.2002), see entire document.	1-25

INTERNATIONAL SEARCH REPORT

International application No.

PCT/US03/29569

Box I Observations where certain claims were found unsearchable (Continuation of Item 1 of first sheet)

This international report has not been established in respect of certain claims under Article 17(2)(a) for the following reasons:

1. Claim Nos.:
because they relate to subject matter not required to be searched by this Authority, namely:

2. Claim Nos.:
because they relate to parts of the international application that do not comply with the prescribed requirements to such an extent that no meaningful international search can be carried out, specifically:

3. Claim Nos.:
because they are dependent claims and are not drafted in accordance with the second and third sentences of Rule 6.4(a).

Box II Observations where unity of invention is lacking (Continuation of Item 2 of first sheet)

This International Searching Authority found multiple inventions in this international application, as follows:
Please See Continuation Sheet

1. As all required additional search fees were timely paid by the applicant, this international search report covers all searchable claims.
2. As all searchable claims could be searched without effort justifying an additional fee, this Authority did not invite payment of any additional fee.
3. As only some of the required additional search fees were timely paid by the applicant, this international search report covers only those claims for which fees were paid, specifically claims Nos.:

4. No required additional search fees were timely paid by the applicant. Consequently, this international search report is restricted to the invention first mentioned in the claims; it is covered by claims Nos.:

Remark on Protest

The additional search fees were accompanied by the applicant's protest.

No protest accompanied the payment of additional search fees.

INTERNATIONAL SEARCH REPORT**PCT/US03/29569****BOX II. OBSERVATIONS WHERE UNITY OF INVENTION IS LACKING**

Group I, claims 1-13, draw to a DEVICE, classified 257, and subclass 678.

Group II, claims 14-25, draw to a method for manufacturing packaged semiconductor devices, classified 438, and subclass 106.

This International Searching Authority considers that the international application does not comply with the requirements of unity of invention (Rules 13.1, 13.2 and 13.3) for the reason indicated below:

The device of group I is different from group II because the Group I deals with a product/device comprising a substrate having first and second generally opposite surfaces, a die mounted to the surfaces, a molding compound encapsulating the die, heat spreader at least partially embedded in the molding compound; and the Group II deals with a method for manufacturing packaged semiconductor devices comprising disposing a plurality of dies onto a plurality of interconnected substrate; electrically connecting I/O pads on each die; securing a plurality of interconnected heat spreaders; over molding the plurality of dies, the bond sites, and the plurality of interconnected heat spreaders with a continuous coating of molding compound to form a plurality of interconnected package precursors.

Continuation of B. FIELDS SEARCHED Item 2:

Lau et al, Electrical Design of a Cost-Effective Plastic GBA Package, IEEE Part B, vol. 21, N0.1, 2/1998.

P. Scharf, T. Coleman and K. Avellar, "Flip Component Technology", IEEE Electronic Component Conference (1967), pp. 269-274. R. Lachance, H. Lavoie, A Mountanari, "Corrosion/Migration Study of Flip Chop Unterfill and Ceramic Overcoating", IEEE Electronic Component and Technology Conference (1997), pp. 885-889.